

REMARKS

Claims 1-27 are pending. The Office Action dated November 8, 2004 has been carefully considered. The above amendments and the following remarks are presented in a sincere attempt to place this Application in condition for allowance. Claims 1, 7, 15, and 16 have been amended in this Response. Claims 11-14 have been withdrawn from consideration in this a previous Response. Reconsideration and allowance are respectfully requested in light of the above amendments and following remarks.

An interview was held with the Examiner, Mr. Vibol Tan, on December 7, 2004 to discuss the rejections under 35 U.S.C. § 103(a) and the proposed amendments thereto. Applicant thanks the Examiner for the courtesies extended.

Claims 16-27 are objected to as being dependent on a rejected base Claim, but would be allowable if rewritten in independent form, including all limitation of the base Claim and any intervening Claim. Claim 16 has been rewritten in independent form to include all limitations of Claim 15. Accordingly, Applicants respectfully request that the objection of Claim 16 as being dependent on a rejected base Claim be withdrawn and that Claim 16 be allowed.

Claims 17-27 depend on and further limit Claim 16. Hence, for at least the aforementioned reasons, these Claims would be deemed to be in condition for allowance. Applicants respectfully requests that the objections of dependent Claims 17-27 also be withdrawn.

Claims 1-10 and 15 stand rejected under 35 U.S.C. §103(a) in view of U.S. Patent No. 5,821,775 by Metha et al. ("Metha") in view of U.S. Patent No. 6,204,696 by Krishnamuthy et al. ("Krishnamuthy"). Insofar as they may be applied against the Claims, these rejections are overcome.

Rejected independent Claim 1 as now amended more particularly recites one of the distinguishing characteristics of the present invention, namely, “generating one or more dynamic signal inputs by dynamically gating the one or more static signal inputs with the clock signal, *wherein the step of generating is at least partially performed by a first AND logic circuit within the logic circuit that is at least configured to receive the clock signal and a static signal input.*” (Emphasis added.) Support for this Amendment can be found, among other places, Figure 1 and page 4, line 8 to page 5, line 7 of the original Application.

Neither Metha nor Krishnamuthy suggest, teach, or disclose generating one or more dynamic signal inputs that are at least partially performed by a first AND logic circuit. Specifically, Metha discloses domino logic that utilizes a phase I domino for a clocking input, and Krishnamuthy also discloses domino logic. However, neither Metha or Krishnamuthy, include detail regarding the use of a AND logic circuit that receives a clock signal and a static signal input to produce dynamic signal input. By using the AND logic in conjunction with the remainder of the present invention of Claim 1, input signals can be true or complement signals. In typical domino logic, such as with Metha and Krishnamuthy, only the transition from 0 to 1 is allowed at the input signals at cascaded domino implementation. In the present invention of Claim 1, either transition (0 to 1 or 1 to 0) is allowed once the setup requirement is met. Thus, there is no compatibility issue in combining the present invention of Claim 1 with static circuits.

In view of the foregoing, it is apparent that the cited referenced do not disclose, teach or suggest the unique combination now recited in amended Claim 1. Applicants therefore submit that amended Claim 1 is clearly and precisely distinguishable over the cited references in a patentable sense, and is therefore allowable over these references and the remaining references of record.

Accordingly, Applicants respectfully request that the rejection of Claim 1 under 35 U.S.C. § 103(a) in view of Metha and Krishnamuthy be withdrawn and that Claim 1 be allowed.

Claims 2-6 depend on and further limit Claim 1. Hence, for at least the aforementioned reasons, these Claims would be deemed to be in condition for allowance. Applicants respectfully request that the rejections of dependent Claims 2-6 also be withdrawn.

Applicants contend that the rejection of Claim 7 is overcome for at least some of the reasons that the rejection of Claim 1 as amended is overcome. These reasons include that neither Metha nor Krishnamuthy disclose, teach, or suggest “means for generating one or more dynamic signal inputs by dynamically gating the one or more static signal inputs with the clock signal, *wherein the step of generating is at least partially performed by a first AND logic circuit within the logic circuit that is at least configured to receive the clock signal and a static signal input.*” (Emphasis added.)

In view of the foregoing, it is apparent that the cited references do not disclose, teach or suggest the unique combination now recited in amended Claim 7. Applicants therefore submit that amended Claim 7 is clearly and precisely distinguishable over the cited references in a patentable sense, and is therefore allowable over these references and the remaining references of record. Accordingly, Applicants respectfully request that the rejection of Claim 7 under 35 U.S.C. § 103(a) in view of Metha and Krishnamuthy be withdrawn and that Claim 7 be allowed.

Claims 8-10 depend on and further limit Claim 1. Hence, for at least the aforementioned reasons, these Claims would be deemed to be in condition for allowance. Applicants respectfully request that the rejections of dependent Claims 7-10 also be withdrawn.

Applicants contend that the rejection of Claim 15 is overcome for at least some of the reasons that the rejection of Claim 1 as amended is overcome. These reasons include neither

Metha nor Krishnamuthy disclosing, teaching, or suggesting “the clock gating circuit further comprises a first AND logic circuit for receiving the clock signal and the first static signal input and for generating a first dynamic signal input.”

In view of the foregoing, it is apparent that the cited references do not disclose, teach or suggest the unique combination now recited in amended Claim 15. Applicants therefore submit that amended Claim 15 is clearly and precisely distinguishable over the cited references in a patentable sense, and is therefore allowable over this reference and the remaining references of record. Accordingly, Applicants respectfully request that the rejection of Claim 15 under 35 U.S.C. § 103(a) in view of Metha and Krishnamuthy be withdrawn and that Claim 15 be allowed.

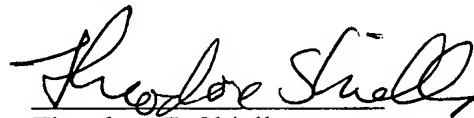
Applicant has now made an earnest attempt to place this Application in condition for allowance. For the foregoing reasons and for other reasons clearly apparent, Applicant respectfully requests full allowance of Claims 1-10 and 15-27.

Applicants do not believe that any fees are due; however, in the event that any fees are due, the Commissioner is hereby authorized to charge any required fees due (other than issue fees), and to credit any overpayment made, in connection with the filing of this paper to Deposit Account No. 50-0605 of CARR LLP.

Should the Examiner require any further clarification to place this application in condition for allowance, the Examiner is invited to telephone the undersigned at the number listed below.

Respectfully submitted,

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